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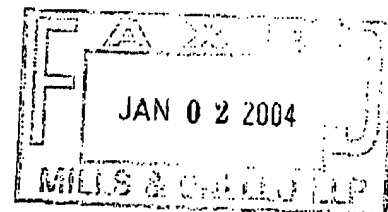
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URGENT



FACSIMILE TRANSMISSION

COPY

TO: United States Patent and Trademark Office
Office of Initial Patent Examination's Filing Receipt Corrections
Group Art Unit: 2811

FROM: Anthony P. Onello, Jr.

FAX NO: (703) 746-9195

RE: U.S. Patent Application
"Frequency Multiplier and Method of Multiplying Frequency
of External Clock Signal, Data Output Buffer, and Semiconductor
Device Including the Frequency Multiplier and the Data Output
Buffer," by Kyoung-hwan Kwon, *et al.*
U.S. Serial No.: 10/671,105
U.S. Filing Date: September 25, 2003
Our Reference: SAM-0474

NUMBER OF PAGES INCLUDING COVER SHEET: 6

REMARKS:

Dear Sirs:

Please find attached a Request for Correction to Filing Receipt for the
referenced patent application.

Respectfully submitted,

Anthony P. Onello, Jr.

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PATENT
Attorney Docket No.: SAM-0474
Customer No.: 29344

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kyoung-hwan Kwon, *et al.* Examiner: Not yet assigned
Serial No.: 10/671,105 Group Art Unit: 2811
Filing Date: September 25, 2003
Title: FREQUENCY MULTIPLIER AND METHOD OF MULTIPLYING FREQUENCY
OF EXTERNAL CLOCK SIGNAL, DATA OUTPUT BUFFER, AND
SEMICONDUCTOR DEVICE INCLUDING THE FREQUENCY MULTIPLIER
AND THE DATA OUTPUT BUFFER

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Date

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Vanessa Marakas
Vanessa Marakas

Office of Initial Patent Examination's Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

TRANSMITTAL LETTER

Sir:

Enclosed herewith for filing in the above-identified patent application please find the following listed items:

1. Request For Correction to Filing Receipt; and
2. Copy of Filing Receipt;

In connection with the foregoing matter, please charge any additional fees which may be due, or credit any overpayment, to Deposit Account Number 50-1798. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

Date: January 2, 2004
MILLS & ONELLO, LLP
Eleven Beacon Street, Suite 605
Boston, Massachusetts 02108
Telephone: (617) 994-4900
Facsimile: (617) 742-7774
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Anthony P. Onello, Jr.
Anthony P. Onello, Jr.
Registration Number 38,572
Attorney for Applicant

PATENT
Attorney Docket No.: SAM-0474

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kyoung-hwan Kwon, *et al.* Examiner: Not yet assigned
Serial No.: 10/671,105 Group Art Unit: 2811
Filing Date: September 25, 2003
Title: FREQUENCY MULTIPLIER AND METHOD OF MULTIPLYING
FREQUENCY OF EXTERNAL CLOCK SIGNAL, DATA OUTPUT BUFFER,
AND SEMICONDUCTOR DEVICE INCLUDING THE FREQUENCY
MULTIPLIER AND THE DATA OUTPUT BUFFER

CERTIFICATE OF FACSIMILE TRANSMISSION

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Facsimile Number

Vanessa Marakas
Vanessa Marakas

Office of Initial Patent Examination's Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

REQUEST FOR CORRECTION TO FILING RECEIPT

Sir:

The Official Filing Receipt received in connection with the above-identified patent application is believed to be in error. The word "Buffer" is missing from the end of the title. The correct title should be "Frequency Multiplier and Method of Multiplying Frequency of External Clock Signal, Data Output Buffer, and Semiconductor Device Including the Frequency Multiplier and the Data Output Buffer". A copy of the incorrect Filing Receipt with the requested change noted thereon is enclosed.

Respectfully submitted,

Date: January 2, 2004
MILLS & ONELLO, LLP
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Telephone: (617) 994-4900
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J:\SAM\0474\reqcorr\fillingrec.wpd

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UNITED STATES PATENT AND TRADEMARK OFFICE

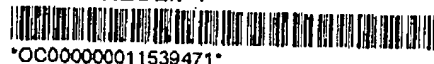
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/671,105	09/25/2003	2811	1128	SAM-0474	6	27	6

Anthony P. Onello, Jr.
MILLS & ONELLO LLP
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CONFIRMATION NO. 7243

FILING RECEIPT



OC000000011539471

Date Mailed: 12/19/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Kyoung-Hwan Kwon, Seoul, KOREA, REPUBLIC OF;
Hyun-Soon Jang, Seoul, KOREA, REPUBLIC OF;
Kyu-Hyoun Kim, Suwon-city, KOREA, REPUBLIC OF;

Assignment For Published Patent Application

Samsung Electronics Co., Ltd.;

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 02-59831 10/01/2002

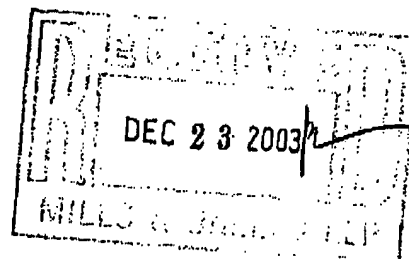
If Required, Foreign Filing License Granted: 12/19/2003

Projected Publication Date: 04/01/2004

Non-Publication Request: No

Early Publication Request: No

Title



Frequency multiplier and method of multiplying frequency of external clock signal, data output buffer, and semiconductor device including the frequency multiplier and the data output *buffer*

Preliminary Class

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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